

Design Of Low Power Coarse Grained Reconfigurable Architectures

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A Hybrid Partially Reconfigurable Overlay Supporting Just

January 22nd, 2019 - The reconfigurable computing community is seeking solutions to raise the level of design abstraction at which programmers must operate and move the synthesis process out of the programmers' path through the use of overlays

Parallel computing Wikipedia

February 21st, 2019 - Parallel computing is a type of computation in which many calculations or the execution of processes are carried out simultaneously Large problems can often be divided into smaller ones which can then be solved at the same time There are several different forms of parallel computing bit level instruction level data and task parallelism

GLSVLSI 2018 Chicago Illinois USA

February 12th, 2019 - Keynote 5 A Cross Layer Perspective for Energy Efficient Processing From beyond CMOS devices to deep learning Water Tower AB Floor 2 9 00 10 00 Friday May 25

Tsinghua University

February 20th, 2019 - 1 Funding Organization National Natural Science Foundation of China NSFC Program Key Program Title Key Technologies of Dynamic Reconfigurable Chip

Amano Lab

February 19th, 2019 - Best Paper Award Yu Fujita Hayate Okuhara Koichiro Masuyama and Hideharu Amano Power optimization considering the chip temperature of low power reconfigurable accelerator CMA SOTB Proc

Field programmable gate array Wikipedia

February 16th, 2019 - A field programmable gate array FPGA is an integrated circuit designed to be configured by a customer or a designer after manufacturing " hence the term field programmable The FPGA configuration is generally specified using a hardware description language HDL similar to that used for an application specific integrated circuit ASIC Circuit diagrams were previously used to specify

USENIX ATC 18 Technical Sessions USENIX

February 16th, 2019 - Dahlia is an applied and foundational researcher since the early nineties in broad aspects of reliability and security in distributed systems

Quality of Service QoS in Software Defined Networking

February 19th, 2019 - Supporting end to end Quality of Service QoS in existing network architectures is an ongoing problem Although researchers from both academia and industry have proposed many solutions to solve the QoS limitations of the current networking many of them either failed or were not implemented

Peer Reviewed Journal IJERA com

February 18th, 2019 - International Journal of Engineering Research and Applications IJERA is an open access online peer reviewed international journal that publishes research

EMBC 18 Program Thursday July 19 2018

February 20th, 2019 - EMBC'18 40th Annual International Conference of the IEEE Engineering in Medicine and Biology Society Hilton Hawaiian Village Waikiki Beach Resort Honolulu USA

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